SecurityTube Linux Assembly Expert (SLAE^{64})

SecurityTube Linux Assembly Expert

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Vivek Ramachandran
SWSE, SMFE, SPSE, SGDE, SISE, SLAE^{32,64} Course Instructor

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Module 1: 64-Bit ASM on Linux

11. Moving Data

Vivek Ramachandran
SWSE, SMFE, SPSE, SGDE, SISE, SLAE^{64}, SLAE^{32} Course Instructor

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Super Important!

When in 64-bit mode, operand size determines the number of valid bits in the destination general-purpose register:

- 64-bit operands generate a 64-bit result in the destination general-purpose register.
- 32-bit operands generate a 32-bit result, zero-extended to a 64-bit result in the destination general-purpose register.
- 8-bit and 16-bit operands generate an 8-bit or 16-bit result. The upper 56 bits or 48 bits (respectively) of the destination general-purpose register are not modified by the operation. If the result of an 8-bit or 16-bit operation is intended for 64-bit address calculation, explicitly sign-extend the register to the full 64-bits.

Because the upper 32 bits of 64-bit general-purpose registers are undefined in 32-bit modes, the upper 32 bits of any general-purpose register are not preserved when switching from 64-bit mode to a 32-bit mode (to protected mode or compatibility mode). Software must not depend on these bits to maintain a value after a 64-bit to 32-bit mode switch.

Intel Manual 3.4.1.1

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MOV

- Most common instruction in ASM

- Allowed Directions
  - Between Registers
  - Memory to Register and Register to Memory
  - Immediate Data to Register
  - Immediate Data to Memory

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LEA

- Load Effective Address – load pointer values
- LEA RAX, [label]

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XCHG

- Exchanges (swaps) Values
- XCHG Register, Register
- XCHG Register, Memory

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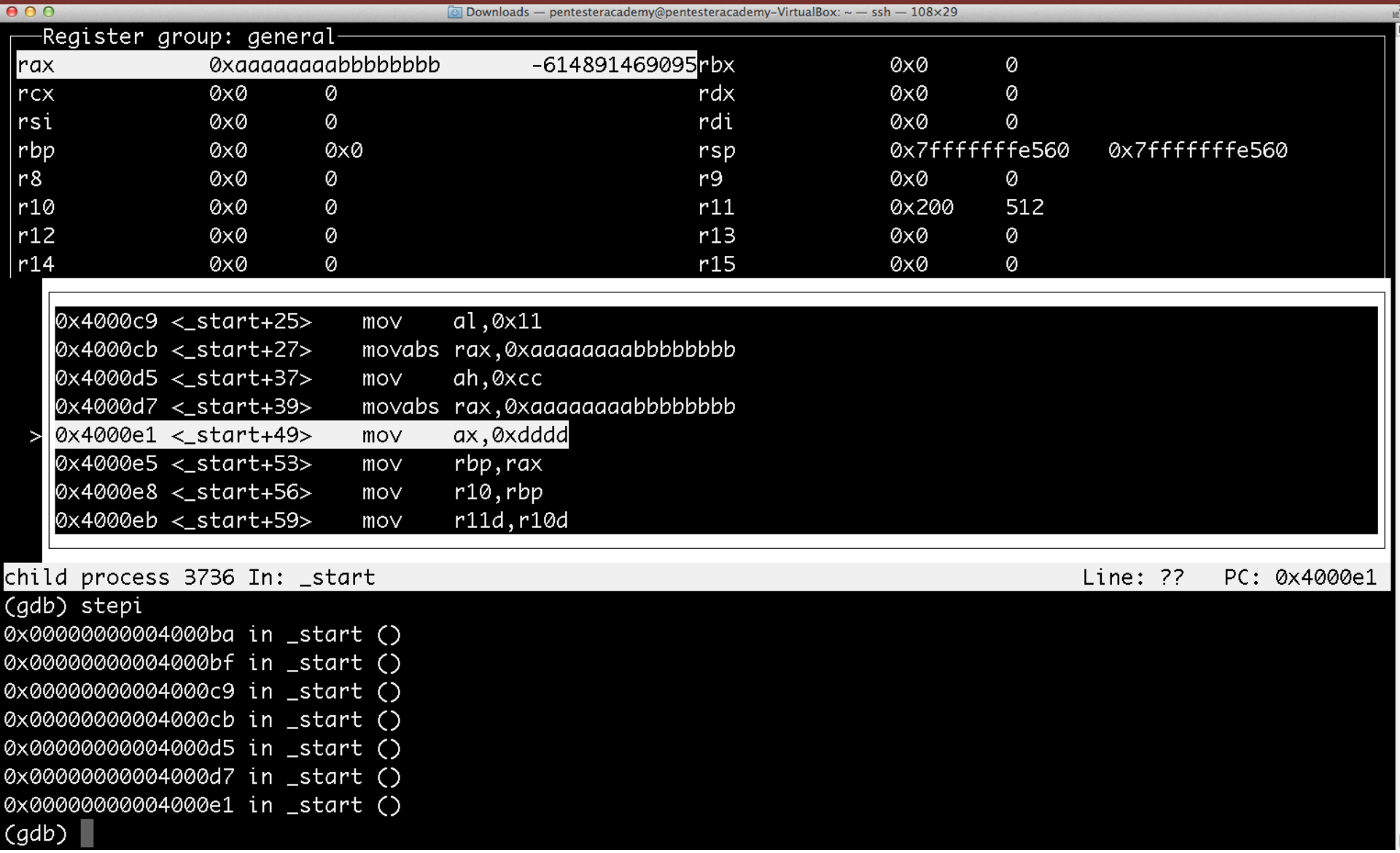
Moving Data Lab



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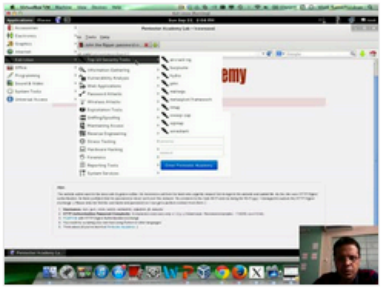
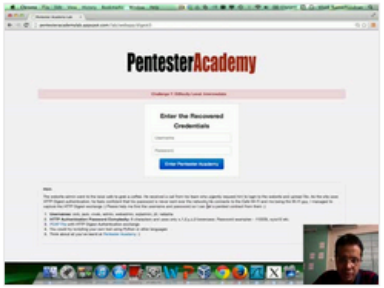


Challenge 7: Cracking Digest Authentication Solution in WAP Challenges



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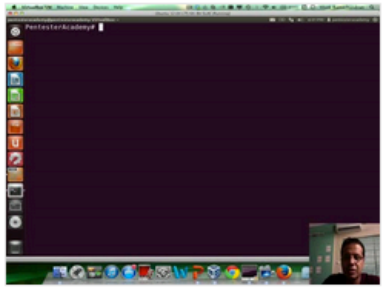
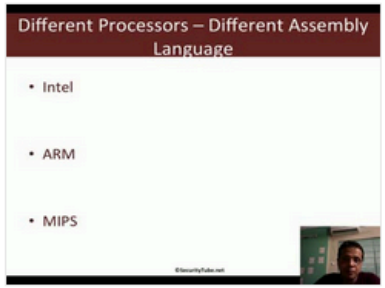


Module 1: GDB Test Solution in x86_64 Assembly Language and Shellcoding on Linux



Module 1: CPU Information in x86_64 Assembly Language and Shellcoding on Linux

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